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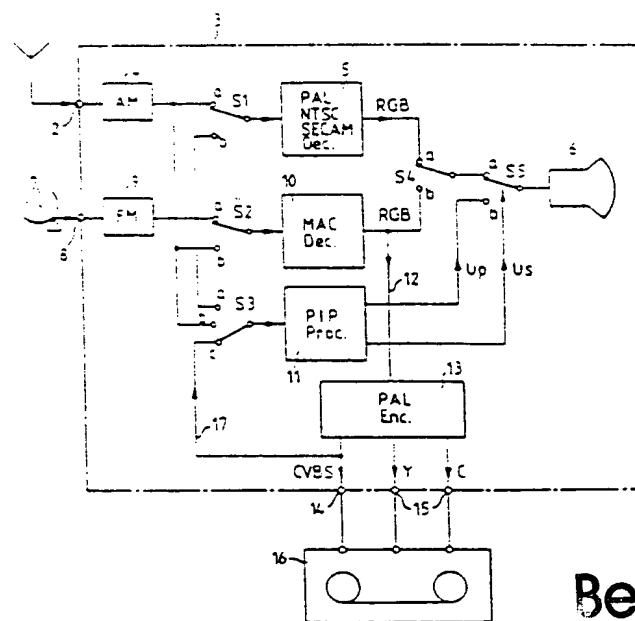
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(54) Title: TELEVISION RECEIVER INCLUDING A PICTURE-IN-PICTURE PROCESSOR



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(57) Abstract

Within a television receiver adapted for MAC reception and including a PIP processor in case the MAC signal is fed to the PIP processor a MAC decoder is needed as the PIP processor can handle only the composite PAL video signal. It is an object to simplify the circuit means in case of PIP display of a MAC signal. According to the invention switching means S3 are provided for connecting the input of the PIP processor to the output of a PAL encoder which is provided for furnishing output signals for a video tape recorder. Especially for a colour television receiver designed for MAC reception and PIP display.

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Television receiver including a picture-in-picture processor.

The invention relates to a television receiver including a PIP processor. This invention is described in terms of the MAC and PAL systems equally applicable to any two different composite or component television systems. Within a television receiver with PIP (picture in picture) reproduction a first video signal which may be received by an antenna is used for displaying the so-called main picture on the screen of the picture tube. Additionally a second video signal which may come from a second antenna via a second tuner or from any other video source connected to the receiver is used for additionally displaying a so-called sub-picture constituting a small picture inserted into the main picture. The second video signal is fed to a so-called PIP processor modifying the video signal for production of the small sub-picture. Said processor essentially includes memory circuits for selecting special lines and time compressing the video signals of said lines.

On the other hand there are television receivers having additionally to the normal circuits a special decoder for decoding special or encrypted signals. One example for such a special signal is the MAC signal. In case said MAC signal shall be used for the small sub-picture than the PIP processor additionally to its normal PIP functions has to decode the MAC signal. In this case, therefore, the PIP processor becomes rather complicated and expensive.

It is an object of the invention to modify the television receiver in such a way that even in case the special encoded or encrypted signal is used for the sub-picture the PIP processor does not need an additional encoder for said special signal.

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According to the invention within a television receiver including a PIP processor an an extra decoder for specific coded or encrypted signals said decoder having outputs used for picture reproduction and further being connected to inputs of an encoder having Y/C and/or CVBS outputs being connected to output terminals of the receiver dedicated for connecting a video recorder thereto switching means are provided for connecting said Y/C or CVBS output to the input of said PIP processor.

The invention, therefore, makes use of an encoder preferable a PAL encoder provided within the television receiver for furnishing signals for an external video recorder connected to the receiver for simplifying the PIP processor in case a special signal like a MAC signal or an encrypted signal is intended to be used for displaying the small sub-picture. By this way it is achieved that the decoder for the special signal must be provided only once within the receiver whereas the PIP processor itself does not need the specific decoder. In other words, an output terminal of the receiver dedicated for connecting a video recorder to the receiver is fed back into the receiver for additionally feeding the PIP processor in case the sub-picture is derived from said special or encrypted signal. If a special fee is to be paid for a decryption or descrambling within the specific decoder said fee has to be paid only once because for the video recorder connected to the receiver on the one hand and the specific decoding for the PIP processor on the other hand only one specific decoder is needed.

Said special signal preferably is a MAC signal. It may also be an encrypted or scrambled signal being not a MAC signal, e.g. an encrypted PAL, SECAM or NTSC signal. The special signal also may be a MAC signal being additionally encrypted or scrambled.

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1 In order that the invention may more readily be understood, a description is now given by way of example only, reference being made to the drawing. The single Figure of the drawing shows in simplified form a television receiver with means 5 for additionally handling a MAC signal and provided for PIP-reproduction. The Figure shows signals applied to the receiver from two antennae, one of them being a satellite dish, the other is shown as an antenna for terrestrial reception. This is by way of an example only, and the signals can 10 originate from radiating or conducting sources, such as satellites, terrestrial transmitters, cable systems, video recorders using tapes or discs, these sources being used in any combination.

15 Within the Figure a first antenna 1 for terrestrial reception is connected to input terminal 2 of television receiver 3. Terminal 2 is connected via the first AM tuner 4 and switch S1 to a decoder 5 which may be a PAL NTSC or SECAM decoder. Decoder 5 furnishes RGB signals fed via switches 20 S4, S5 to picture tube 6 for reproduction. The normally provided video output stages before the tube 6 are not shown for ease of simplification. A second antenna 7 in form of a dish for microwave reception is connected to second input terminal 8 which is connected via a second FM tuner 9 and switch S2 to a MAC decoder 10. Decoder 10 also provides RGB signals being switchable to tube 6 via switches S4, S5. Switches S1, S2 are arranged to connect each of signals at terminals 2, 8 to decoders 5 or 10 in correspondence with the form of signal received by antennae 1 or 7.

25 Outputs of tuners 4, 9 additionally are connectable to input of a PIP processor 11. Processor 11 provides a signal Up for reproduction of a small sub-picture. Processor 11 generates a so-called fast switching voltage Us actuating switch S5.

30 During display of the sub-picture switch S5 is switched by

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1 Us into position b so that instead of signal from decoder 5 or 10 signal Up from processor 11 is inserted. For example the main picture on tube 6 is derived from a MAC signal received by antenna 7 switch S2 being in position a, switch S4 in position b and switch S5 in position a. PIP processor 11 on the other hand simultaneously is fed by a signal received from antenna 1 with switch S3 being in position b.

If, however, the sub-picture made by Up shall be derived from a received MAC signal, switch S3 being in position a, then the processor 11 additionally needs to include an additional specific decoder for MAC. Said additional decoder within processor 11 is avoided in the following way.

18 RGB signals from MAC decoder 10 used for picture reproduction additionally are fed via leads 12 to PAL encoder 13. Encoder 13 provides at output terminal 14 a composite PAL signal CVBS and at output terminals 15 additionally a Y/C signal at different leads, Y being the luminance signal and C the chroma signal. Output terminals 14, 15 are provided for connecting a video recorder 16 thereto, alternatively to terminal 14 or to terminal 15. CVBS signal at terminal 14 is used for a normal domestic video recorder whereas terminals 15 are used for a domestic video recorder working according to the Y/C principle. Terminal 14 ordinary being an output terminal only for connecting a video recorder thereto is fed via lead 17 to one input of switch S3. In case a received MAC signal shall be used for the sub-picture switch S3 is switched into position c so that PIP processor 11 receives the normal CVBS signal which is already MAC decoded and/or additionally descrambled within decoder 10. The specific MAC decoder 10 which may also include a decryption or descrambling circuit therefore additionally is used for providing a normal signal not MAC encoded and not encrypted for PIP processor 11.

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In the example shown decoder 10 provides RGB signals. Likewise decoder 10 may provide signals in the form Y/U, V or a composite PAL video signal CVBS. Decoder 5 may likewise work for a PAL, a NTSC or a SECAM signal. In the same way encoder 13 may be instead of a PAL encoder a SECAM encoder or a NTSC encoder for connecting a video tape recorder for said signals to receiver 3.

CLAIMS

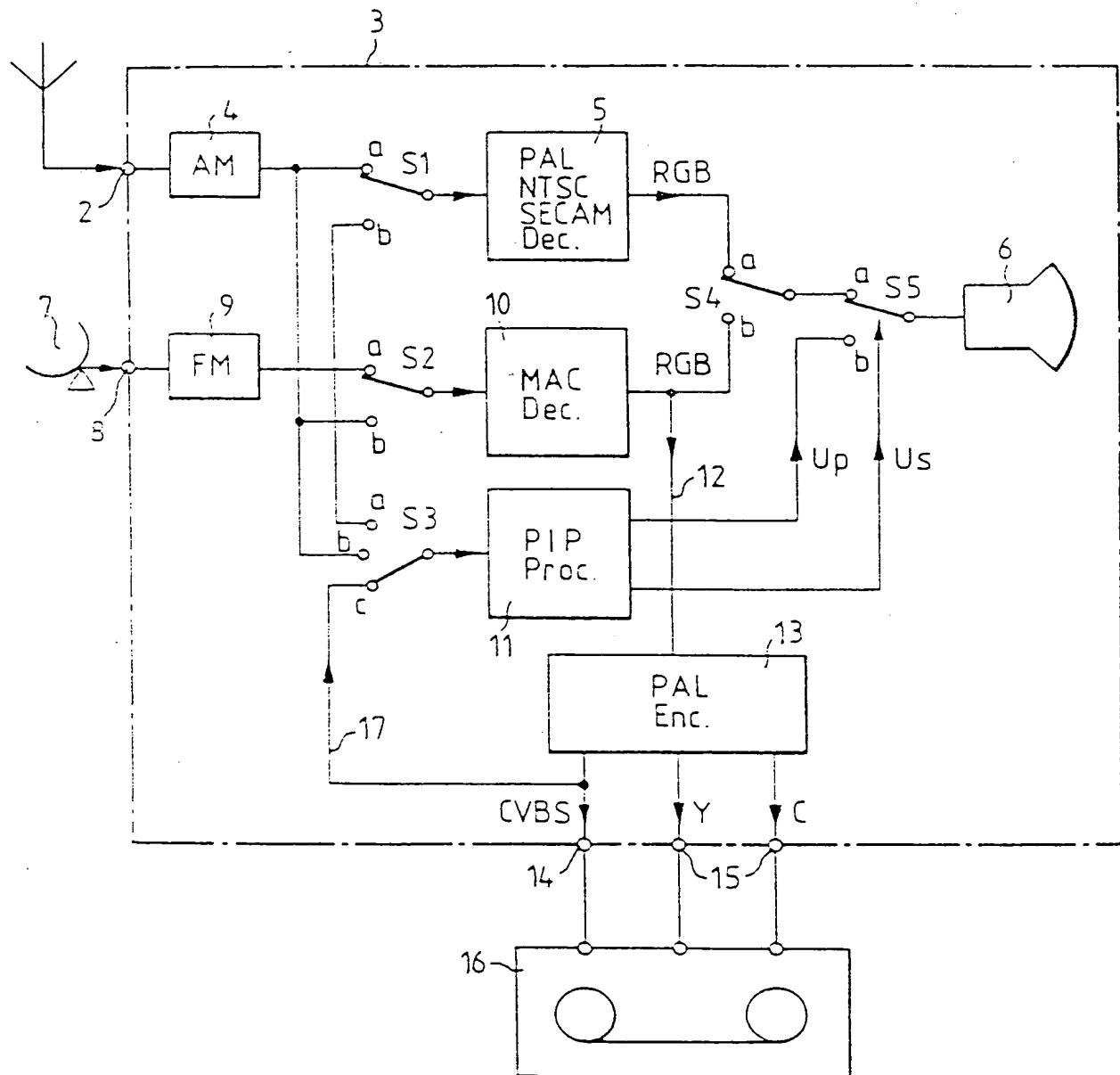
1. Television receiver including a PIP processor and an extra decoder for a specific coded or encrypted signal said decoder having outputs used for picture reproduction and further being connected to inputs of an encoder having Y/C and/or CVBS outputs being connected to output terminals of the receiver dedicated for connecting a video recorder there-to, characterized by switching means (S3) for connecting said Y/C or CVBS output (14,15) to the input of said PIP processor (11).
2. Receiver according to Claim 1, characterized in that said special coded signal is a MAC signal.
3. Receiver according to Claim 2, characterized in that said MAC signal is an encrypted signal.
4. Receiver according to Claim 1, characterized in that said encoder (13) is a PAL encoder.
5. Receiver according to Claim 1, characterized in that said Y/C or CVBS outputs are component Y/C and/or CVBS outputs.
6. Receiver according to Claim 1, characterized in that the outputs of said decoder (10) are RGB outputs.
7. Receiver according to Claim 1, characterized in that the outputs of said decoder (10) are Y, UV or CVBS outputs.
8. Receiver according to Claim 1, characterized in that the output from the said encoder (13) is not available for external connection, but the output from the said

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encoder is used within the receiver for picture and picture processing.

9. Receiver as described within the specification and the drawing.

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## INTERNATIONAL SEARCH REPORT

International Application No. PCT/EP 91/02084

## I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all)

According to International Patent Classification (IPC) or to both National Classification and IPC

IPC5: H 04 N 5/45

## II. FIELDS SEARCHED

Minimum Documentation Searched<sup>1</sup>

Classification System <sup>2</sup>	Classification Symbols
IPC5	H 04 N

Documentation Searched other than Minimum Documentation  
to the Extent that such Documents are Included in Fields Searched<sup>3</sup>

III. DOCUMENTS CONSIDERED TO BE RELEVANT<sup>4</sup>

Category <sup>5</sup>	Citation of Document <sup>11</sup> with indication, where appropriate, of the relevant passages <sup>12</sup>	Relevant to Claim No. <sup>13</sup>
A	US, A, 4682234 (S.V. NAIMPALLY) 21 July 1987, see the whole document	1-9
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A	US, A, 4729027 (HAKAMADA ET AL) 1 March 1988, see column 1, line 55 - line 63	1-9
	---	
A	US, A, 4959719 (STRUBBE ET AL) 25 September 1990, see the whole document	1-9
	---	
A,P	EP, A2, 0409562 (SONY CORPORATION) 23 January 1991, see page 4, line 33 - line 37	1-9
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\* Special categories of cited documents:<sup>10</sup>

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## IV. CERTIFICATION

Date of the Actual Completion of the International Search  
21st January 1992

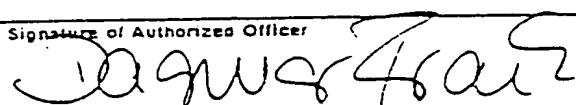
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ANNEX TO THE INTERNATIONAL SEARCH REPORT  
ON INTERNATIONAL PATENT APPLICATION NO.PCT/EP 91/02084

SA 52661

This annex lists the patent family members relating to the patent documents cited in the above-mentioned international search report.  
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Patent document cited in search report	Publication date	Patent family member(s)		Publication date
US-A- 4682234	21/07/87	CA-A-	1247232	20/12/88
		EP-A-B-	0228746	15/07/87
		JP-A-	62224170	02/10/87
US-A- 4729027	01/03/88	AU-D-	6694386	09/07/87
		CA-A-	1265611	06/02/90
		EP-A-	0229032	15/07/87
		JP-A-	62159583	15/07/87
US-A- 4959719	25/09/90	EP-A-	0376376	04/07/90
		JP-A-	2218277	30/08/90
EP-A2- 0409562	23/01/91	JP-A-	3049487	04/03/91

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